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Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

CLAIMS:

 (Withdrawn) A method of image processing an image in a camera module having an image sensor recording an image, said method including the steps of:

capturing an image from said image sensor;

storing said image in image storage memory in a planarized linear RGB form; and transforming said image from planarized linear RGB form to L*a*b* form.

- (Withdrawn) The method of claim 1 further including the step of transmitting said L*a*b* form image
 on a bus of a compact printer system.
- 3. (Withdrawn) The method of claim 1 wherein the step of capturing said image includes the step of linearizing said image from said image sensor.
- 4. (Withdrawn) The method of claim 3 wherein the step of inearizing is performed via a lookup table.
- 5. (Withdrawn) The method of claim 1 further including the step of generating a histogram of image data for each image color plane.
- 6. (Withdrawn) The method of claim 1 wherein the step of transforming said image includes the steps of; white balance and range expansion;

resampling;

color conversion; and

sharpening.

7. (Withdrawn) The method of claim 6 wherein the step of white balance and range expansion requires correction of each pixel value using the formula:

Pixel' = (Pixel - LowThreshold) x RangeScaleFactor

where RangeScaleFactor=256/(HighThreshold - LowThreshold)

- 8. (Withdrawn) The method of claim 6 wherein the step of resampling generates a full RGB form image from available data.
- 9. (Withdrawn) The method of claim 6 wherein the step of color conversion coverts the full RGB form image to an L*a*b* form image.
- 10. (Withdrawn) The method of claim 6 wherein the step of color conversion coverts the full RGB form image to an L*a*b* form image by tri-linear interpolation.
- 11. (Withdrawn) The method of claim 6 wherein the step of sharpening requires highpass filtering of L* data of the L*a*b* form image.
- 12. (Withdrawn) An image processor for a camera module having an image sensor that captures an image, said image processor comprising:

a central processing unit;

program memory associated with said central processing unit, said program memory storing program steps for execution by said central processing unit to operate said camera module to capture said image; one or more interface units communicating with components of said camera module;

image storage memory storing said image;

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transmission by a serial bus interface.

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an image capture unit in communication with said image storage memory, said central processing unit and said image sensor, said image capture unit capturing said image from said image sensor and storing said image in said image storage memory; and image processing unit in communication with said image storage memory to transform said image for

- 13. (Withdrawn) The image processor of claim 12 wherein said image processor is an application specific integrated circuit.
- 14. (Withdrawn) The image processor of claim 12 further comprising scratch memory associated with said central processing unit for variable storage.
- 15. (Withdrawn) The image processor of claim 12 wherein said serial bus interface communicates with a Serial Bus of a compact printer system including one or more further modules, said Serial Bus communicating power and data between said camera module and said one or more further modules.
- 16. (Withdrawn) The image processor of claim 15 wherein said data includes image data transformed by said image processing units.
- 17. (Withdrawn) The image processor of claim 12 wherein said central processing unit is a micro-controller running at about 1 Mhz.
- 18. (Withdrawn)The image processor of claim 12 wherein at least one of said one or more interface units is a parallel interface unit communicating with a take button for initiating capture of an image by said image capture unit.
- 19. (Withdrawn) The image processor of claim 12 wherein said image storage memory is less than 1 Mbytes.
- (Withdrawn) The image processor of claim 19 wherein said image storage memory is approximately0.5 Mbytes.
- (Withdrawn) The image processor of claim 12 wherein said program memory is no greater than 8
 Kbytes.
- 22. (Withdrawn) The image processor of claim 14 wherein said scratch memory is no greater than 2 Kbytes.
- 23. (Withdrawn) The image processor of claim 12 wherein said image processing units include an image histogram unit and an image enhancement unit.

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- 24. (Withdrawn) The image processor of claim 23 wherein said image enhancement unit includes a white balance and range expansion unit, resample unit, color conversion unit and a sharpen unit.
- 25. (New) A hand held apparatus for recording and printing an image, the apparatus comprising: a camera module including an image sensor for recording the image and an image processor for processing the image; and

a page width printer module for printing the processed image, the printer module being releasably engageable with the camera module,

wherein the camera and the printing modules include complementary respective interfacing means configured for transmitting data and power between the camera module and the printer module when the modules are engaged.

- 26. (New) The apparatus according to claim 25, wherein the interface means includes a serial bus interface.
- 27. (New) The apparatus according to claim 26, wherein the image processor comprises: a central processing unit;

program memory associated with said central processing unit, said program memory storing program steps for execution by said central processing unit to operate said camera module to capture said image;

one or more interface units communicating with components of said camera module; image storage memory for storing said image;

an image capture unit in communication with said image storage memory, said central processing unit and said image sensor, said image capture unit capturing said image from said image sensor and storing said image in said image storage memory; and

at least one image processing unit being in communication with said image storage memory, the image processing unit being configured for converting the image from a first color space to a second color space and, when the printing module is engaged to the camera module, transmitting the processed image to the compact page-width printer module by means of the serial bus interface.

- 28. (New) The apparatus of claim 27 wherein said image processor is an application specific integrated circuit.
- 29. (New) The apparatus of claim 27 further comprising scratch memory associated with said central processing unit for variable storage.
- 30. (New) The apparatus of claim 27 wherein said serial bus interface communicates with a Serial Bus of a compact printer system including one or more further modules, said Serial Bus communicating power and data between said camera module and said one or more further modules.
- 31. (New) The apparatus of claim 30 wherein said data includes image data transformed by said image processing units.

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- 32. (New) The apparatus of claim 27 wherein said central processing unit is a micro-controller running at about 1 Mhz.
- 33. (New) The apparatus of claim 27 wherein at least one of said one or more interface units is a parallel interface unit communicating with a take button for initiating capture of an image by said image capture unit.
- 34. (New) The apparatus of claim 27 wherein said image storage memory is less than 1 Mbytes.
- 35. (New) The apparatus of claim 34 wherein said image storage memory is approximately 0.5 Mbytes.
- 36. (New) The apparatus of claim 27 wherein said program memory is no greater than 8 Kbytes.
- 37. (New) The apparatus of claim 29 wherein said scratch memory is no greater than 2 Kbytes.
- 38. (New) The apparatus of claim 27 wherein said image processing units include an image histogram unit and an image enhancement unit.
- 39. (New) The apparatus of claim 38 wherein said image enhancement unit includes a white balance and range expansion unit, resample unit, color conversion unit and a sharpen unit.
- 40. (New) The apparatus of claim 27 wherein the second color space is L*a*b*.